

FORM PTO-1449 (MODIFIED)**LIST OF PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT**

Applicant(s): Arun Kwangil Iyengar
 Docket No.: YOR920010663US1
 Serial No.: 10/629,284
 Filing Date: July 29, 2003
 Group: To Be Assigned

U.S. PATENT DOCUMENTS

<u>EXAMINER</u>	<u>DOCUMENT NO.</u>	<u>DATE</u>	<u>NAME</u>	<u>CLASS/SUBCLASS</u>	<u>FILING DATE</u>
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FOREIGN PATENT DOCUMENTS

<u>EXAMINER</u>	<u>DOCUMENT NO.</u>	<u>DATE</u>	<u>COUNTRY</u>	<u>CLASS/SUBCLASS</u>	<u>TRANSLATION</u>
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OTHER DOCUMENTS

<u>EXAMINER</u>	<u>REF NO.</u>	<u>AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.</u>
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- 1. J. Hennessy et al., "Computer Architecture: A Quantitative Approach," Chapter 6 (6.1, 6.2, 6.10 and 6.11), Morgan Kauffman Publishers, pp. 485-495 and pp. 553-557, 1996.

Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.